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Applicant(s): Claude GAUTHIER *et al.*

Docket No.

03226.065001;P5347

Serial No.

09/819,198

Filing Date

March 28, 2001

Examiner

W. D. Thompson

Group Art Unit

2123

Invention:

LOW-COMPLEXITY, HIGH ACCURACY MODEL OF A CPU ANTI-RESONANCE SYSTEM

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(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
03226.065001;P5347 09-04-03

In Re Application Of: Claude GAUTHIER et al.

Serial No.
09/819,198

Filing Date
March 28, 2001

Examiner
William D. Thompson

Group Art Unit
2123

Title: LOW-COMPLEXITY, HIGH ACCURACY MODEL OF A CPU ANTI-RESONANCE SYSTEM

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37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

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LOW-COMPLEXITY, HIGH ACCURACY MODEL OF A CPU ANTI-RESONANCE SYSTEM

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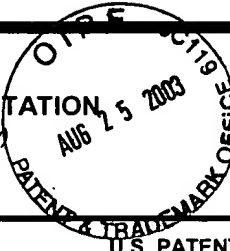
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Inevntor(s): Claude GAUTHIER et al.

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

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FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

B1	"Parasitic Resistance in an MOS Transistor Used as On-Chip Decoupling Capacitance" as published in IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 32, No. 4, April 1997 Author: Patrik Larsson (pages 574-576)
B2	"Power Supply Noise in Future IC's: A Crystal Ball Reading" as published in IEEE 1999 CUSTOM INTEGRATED CIRCUITS CONFERENCE Author: P. Larsson (pages 467-474)

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.